

FIG. 1

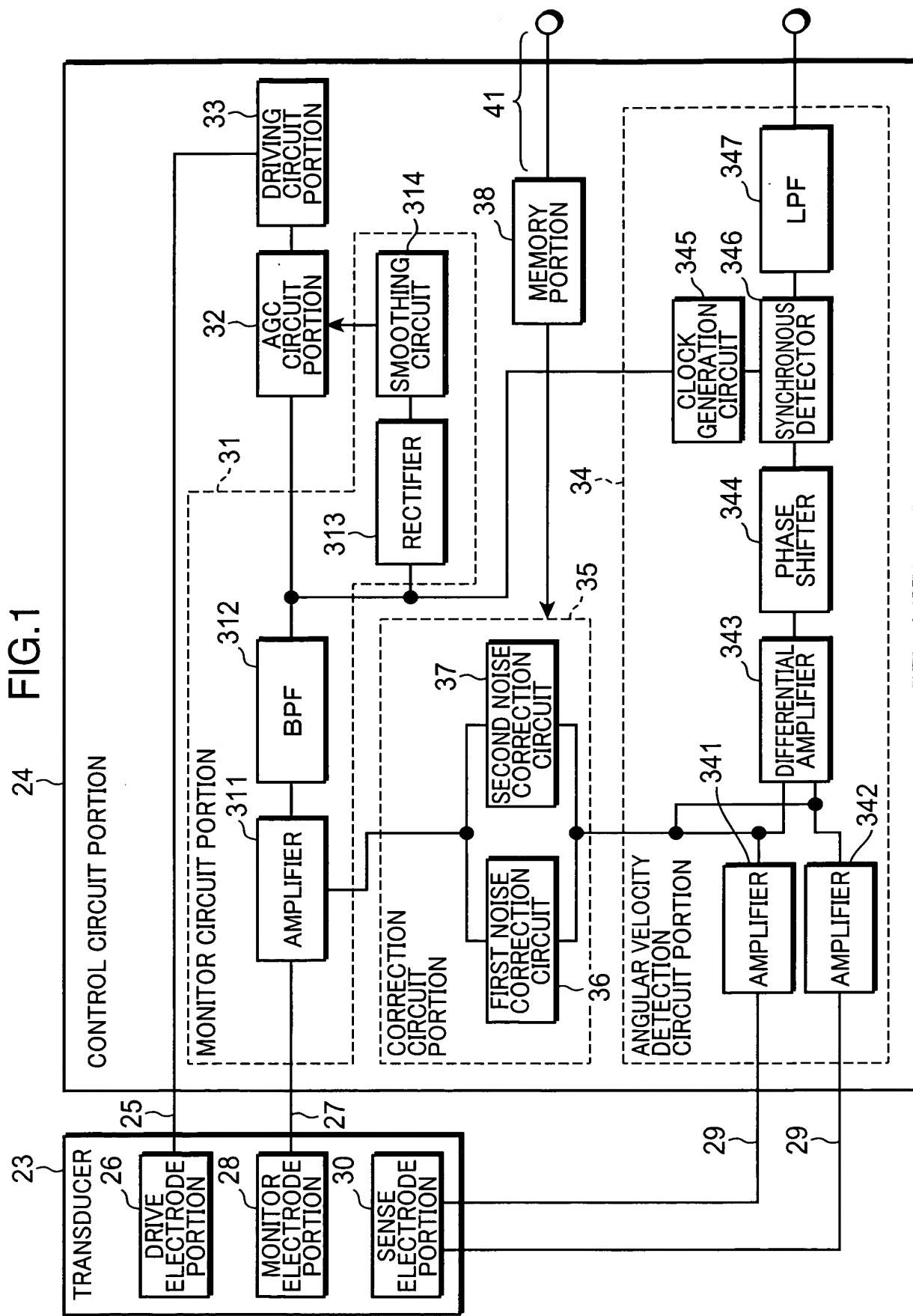
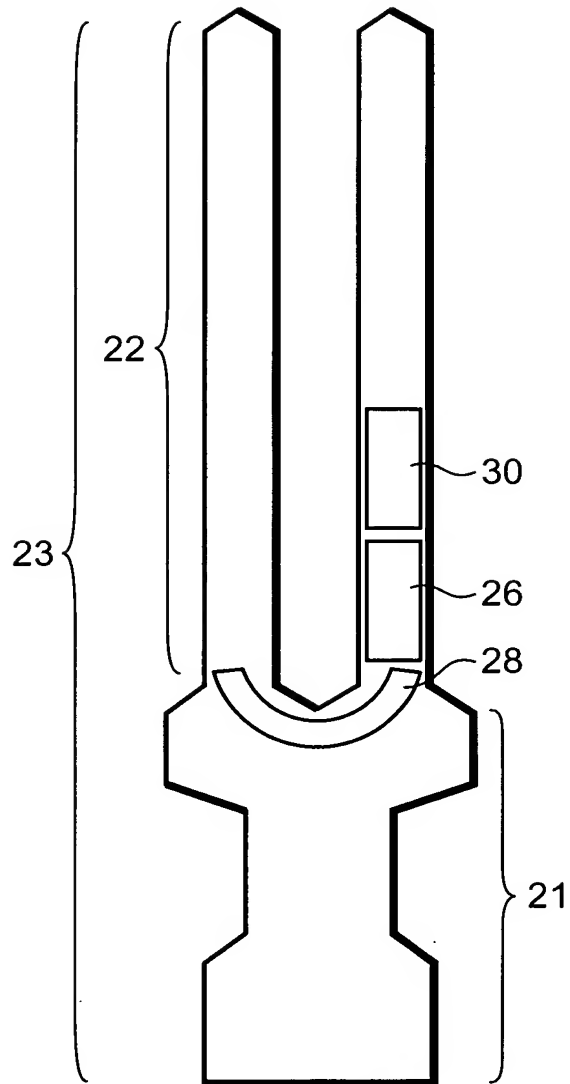


FIG.2



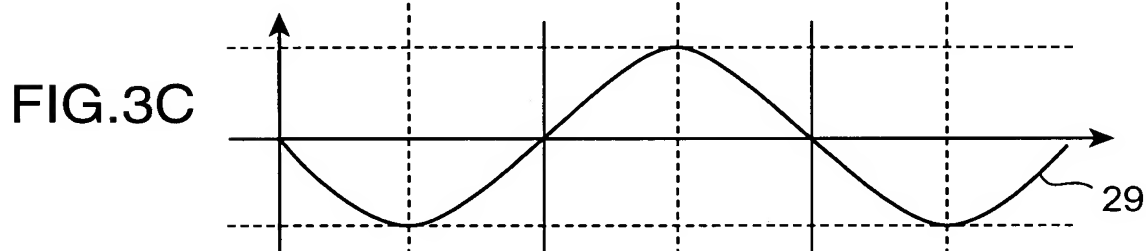
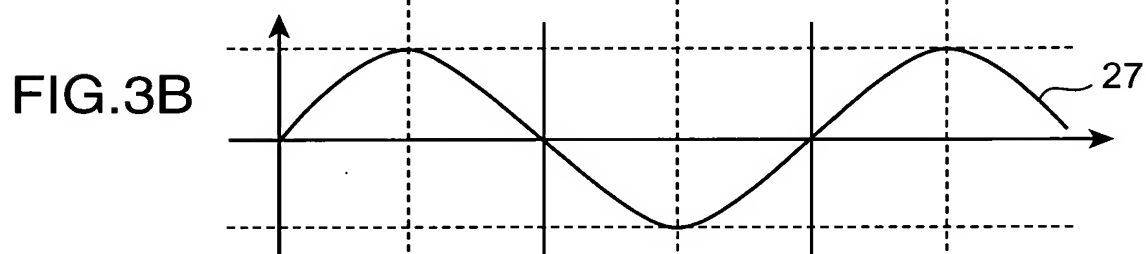
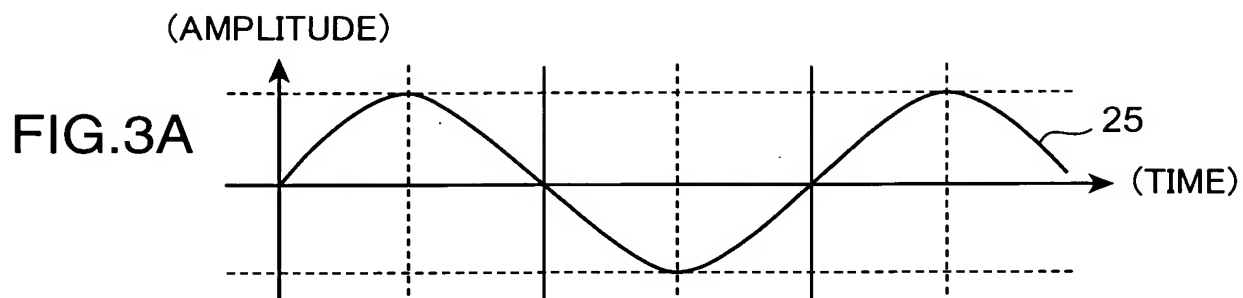
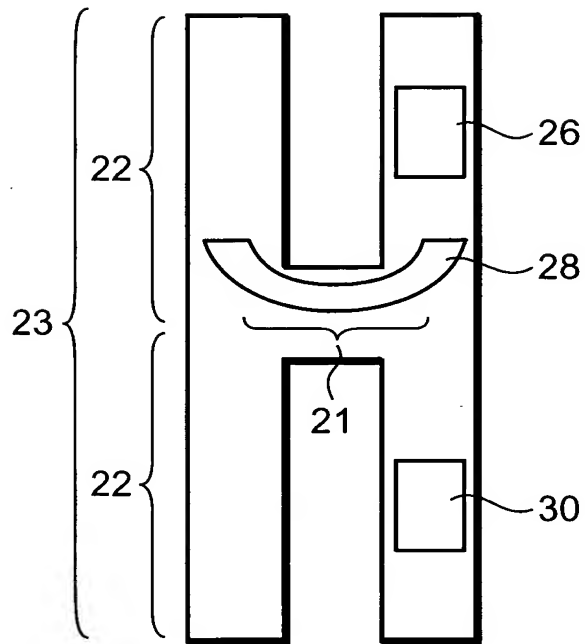


FIG.4



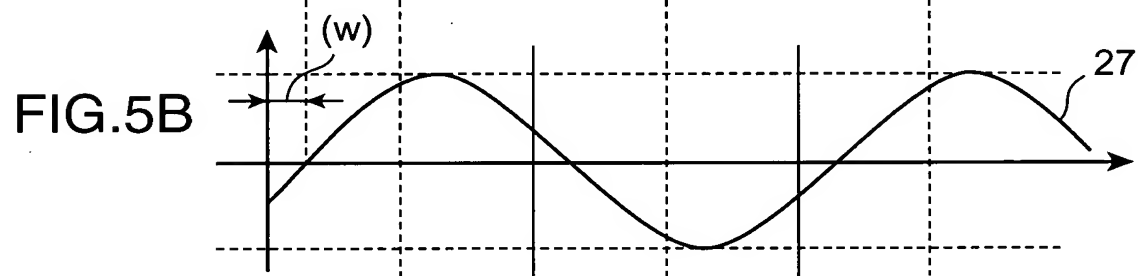
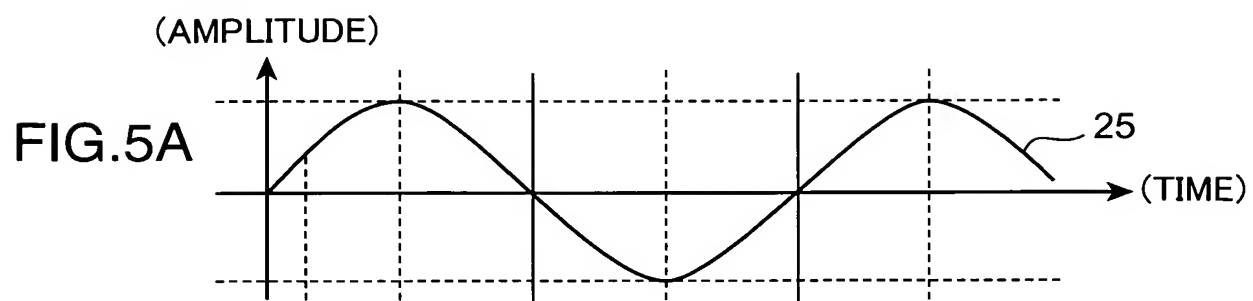
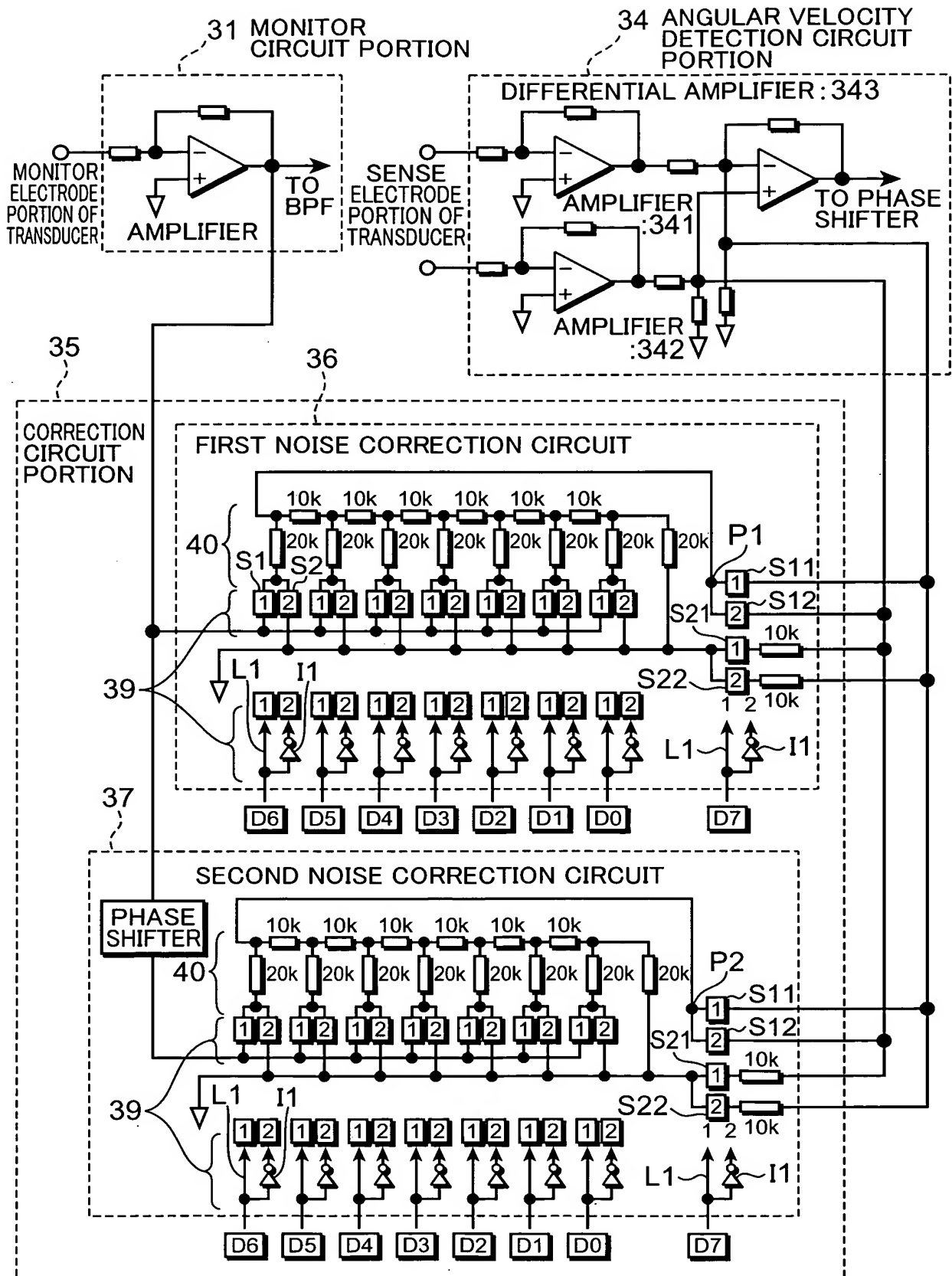


FIG.6



(AMPLITUDE)

FIG.7A
DRIVE SIGNAL
(OUTPUT OF
DRIVING
CIRCUIT
PORTION)

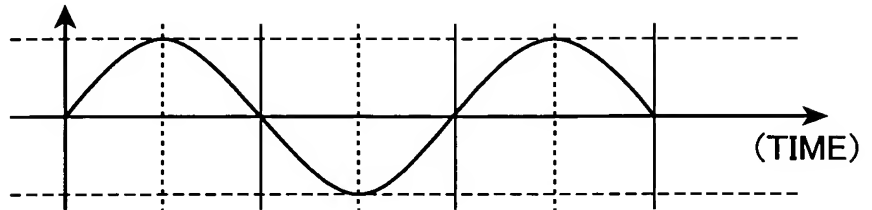


FIG.7B
MONITOR SIGNAL
(OUTPUT OF BPF
IN MONITOR
CIRCUIT PORTION)

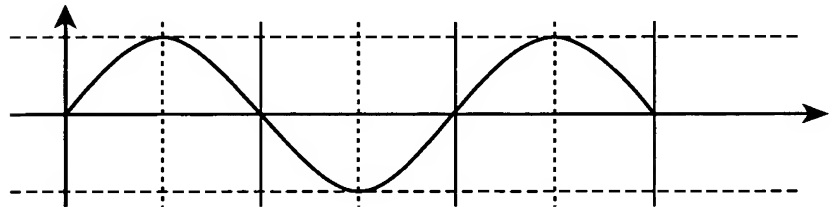


FIG.7C
DIFFERENTIAL
SENSE SIGNAL
(OUTPUT OF
DIFFERENTIAL
AMPLIFIER IN
ANGULAR VELOCITY
DETECTION
CIRCUIT PORTION)

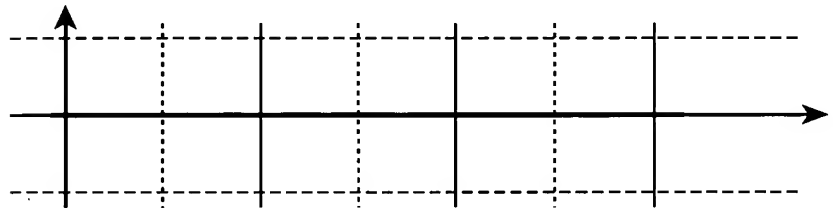


FIG.7D
OUTPUT OF
CLOCK
GENERATION
CIRCUIT

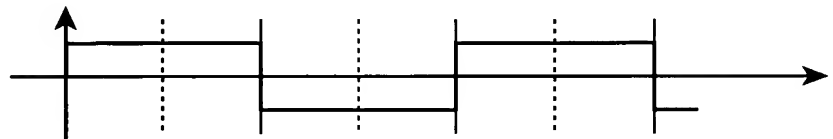


FIG.7E
INPUT OF
SYNCHRONOUS
DETECTOR
(OUTPUT OF
PHASE SHIFTER)

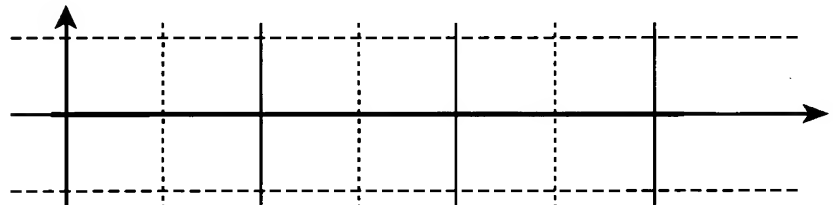


FIG.7F
OUTPUT OF
SYNCHRONOUS
DETECTOR

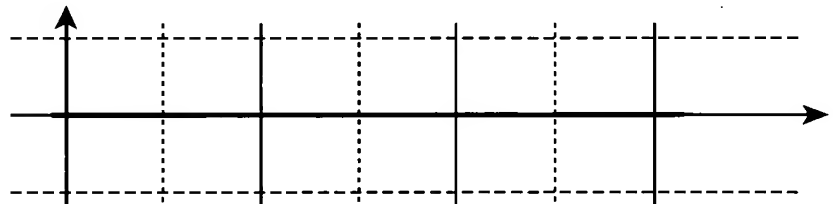
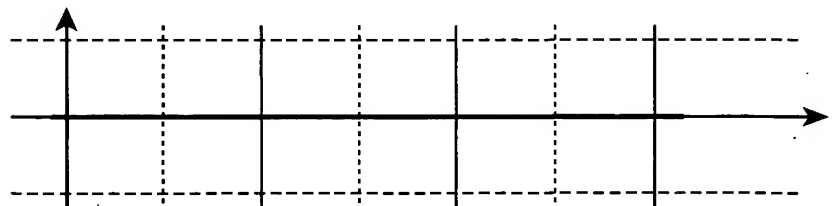


FIG.7G
OUTPUT OF LPF
(ANGULAR
VELOCITY SIGNAL)



(AMPLITUDE)

FIG.8A
DRIVE SIGNAL
(OUTPUT OF
DRIVING
CIRCUIT
PORTION)

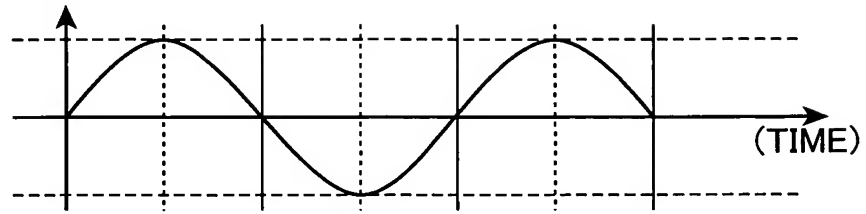


FIG.8B
MONITOR SIGNAL
(OUTPUT OF BPF
IN MONITOR
CIRCUIT PORTION)

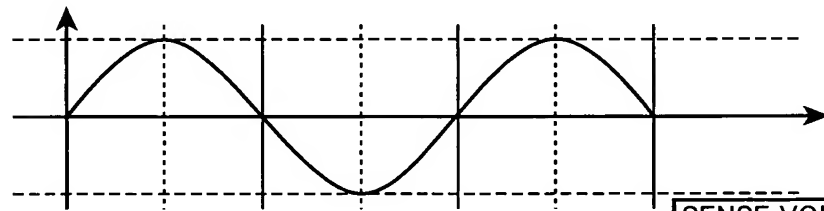


FIG.8C
DIFFERENTIAL
SENSE SIGNAL
(OUTPUT OF
DIFFERENTIAL
AMPLIFIER IN
ANGULAR VELOCITY
DETECTION
CIRCUIT PORTION)

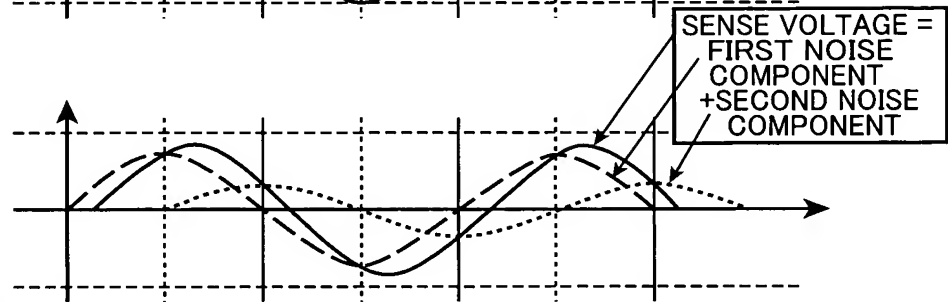


FIG.8D
OUTPUT OF
CLOCK
GENERATION
CIRCUIT

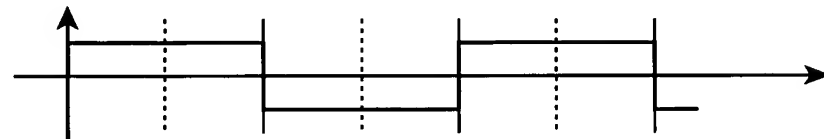


FIG.8E
INPUT OF
SYNCHRONOUS
DETECTOR
(OUTPUT OF
PHASE SHIFTER)

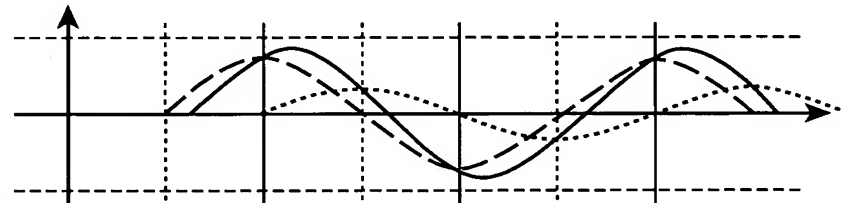


FIG.8F
OUTPUT OF
SYNCHRONOUS
DETECTOR

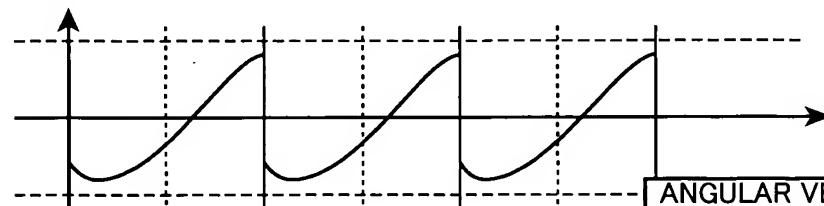
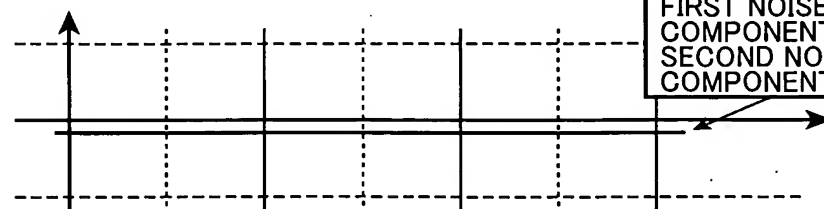


FIG.8G
OUTPUT OF LPF
(ANGULAR
VELOCITY
SIGNAL)



ANGULAR VELOCITY
DETECTION ERROR
OCCURS DUE TO
FIRST NOISE
COMPONENT AND
SECOND NOISE
COMPONENT

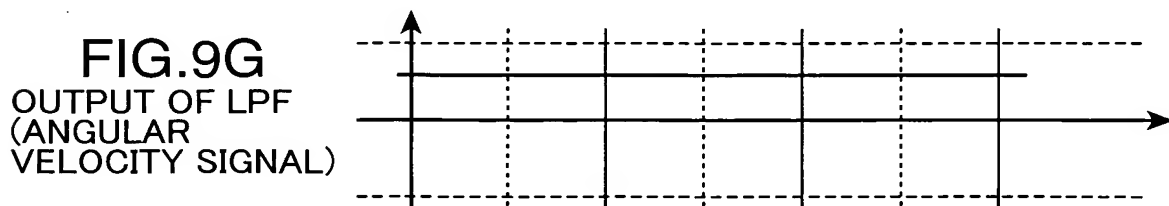
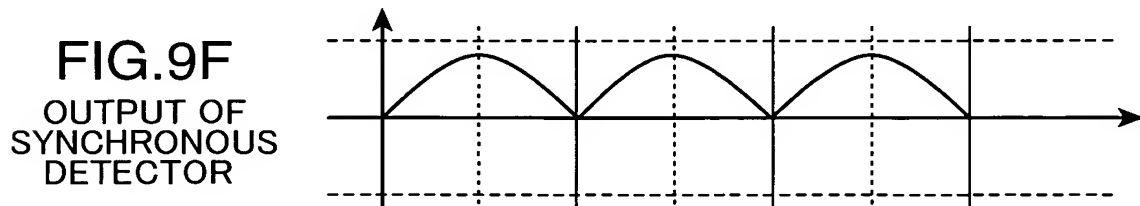
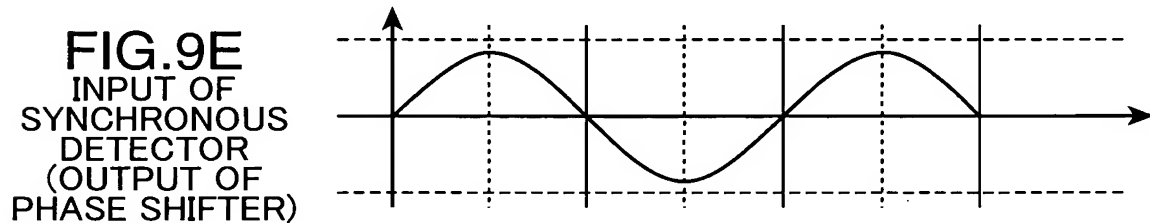
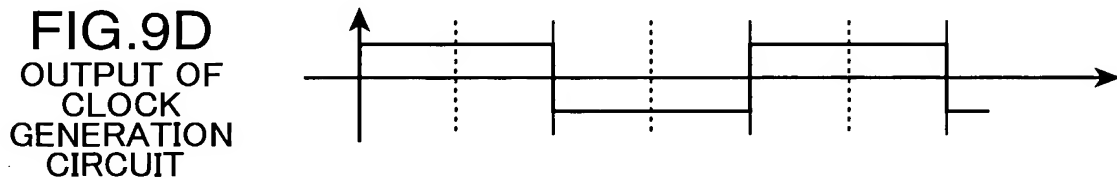
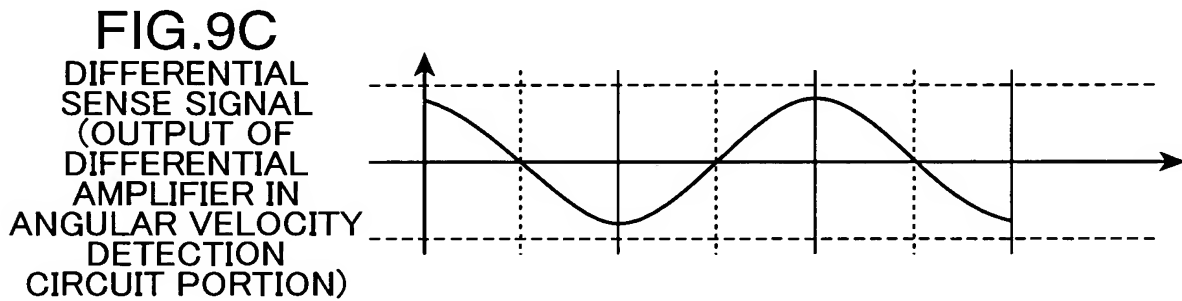
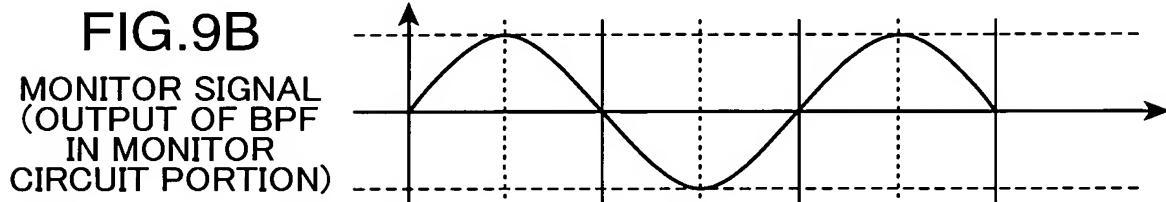
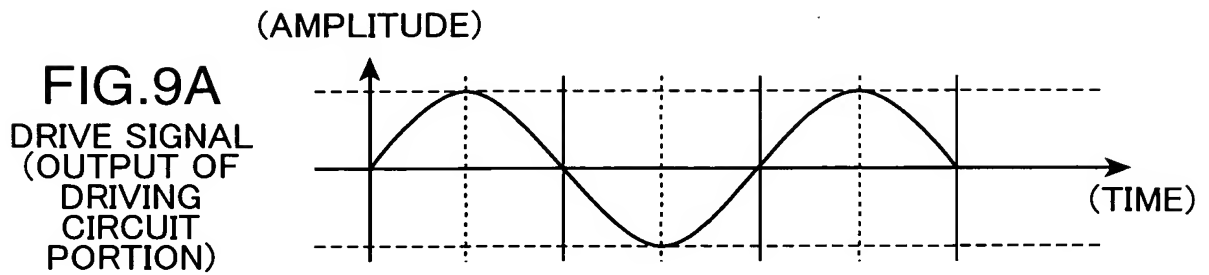


FIG.10

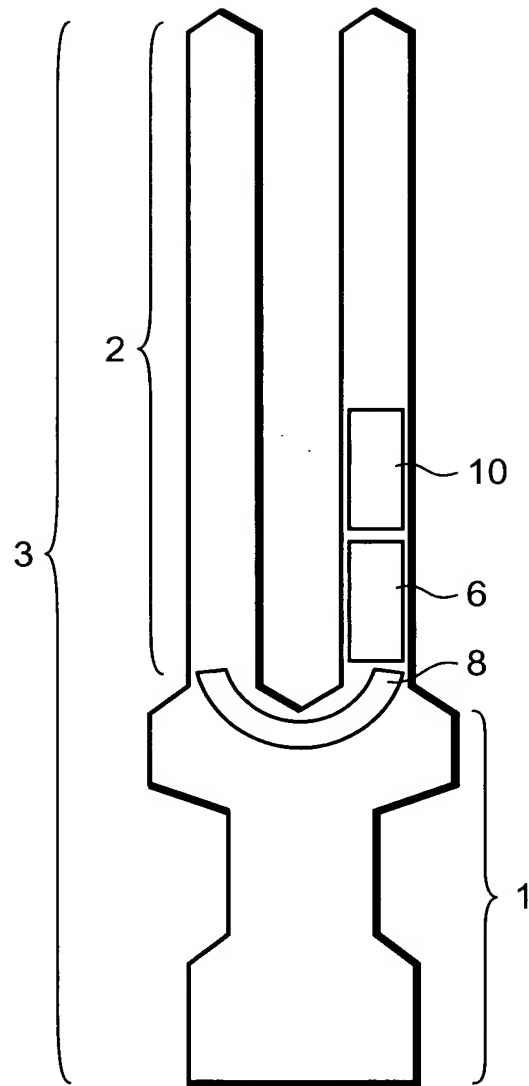


FIG.11

